

4K2K Display Graphics Connector – Mitigating EMI



Introduction – Solder Tail Shielding Mitigates EMI

The display graphics connector's exposed solder tails transfer 5.4 Gbps, radiating EMI at a Nyquist frequency of 2.7 GHz, which interferes with many wireless carrier frequency bands. They need to be shielded. No longer will device solutions require a secondary value-added action (i.e. applying metallized tape or an extra metal cover to be added over the graphics connector) since an integrated solder tail shielding feature shown in Figure 1 has been designed into the display graphics connector.

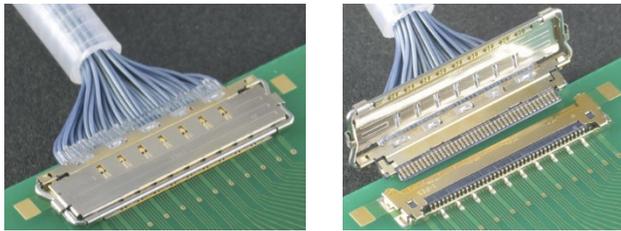


Figure 1. Graphics connector with integrated shielding

Balanced Grounding at the Termination

The differentially driven coaxial wire termination in the high-speed graphics connectors is an internal Faraday-caged wire termination area. The prepared coaxial wire assembly has exposed shields that are soldered to formed grounding bars on the top and bottom creating a solid grounding bar construction by completely filling each fillet (void) between the coaxial wires as shown in Figure 2. This also provides a mechanical strain relief for the center conductor attachment to the connector plug housing with when someone pulls on the cable's wires.



Figure 2. Solid ground bar in housing

Ground fingers provide a shroud between each differentially driven coaxial wire pair. This provides the shortest high-speed path to ground shown by the blue arrows in Figure 3 below.

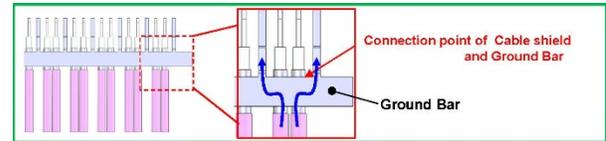


Figure 3. Balanced Shield Ground Paths

The grounding fingers provide a differentially driven coaxial transmission line with the balanced termination needed for acceptable high-speed differential data like the signals shown in Figure 4.

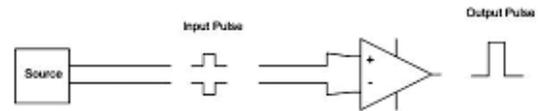


Figure 4. Differential signal has two polarities

An "unbalanced" termination can generate a large amount of differential to common mode conversion (Scd21 – noise - known as Transverse Conversion Transfer Loss) and EMI. Figure 5 shows a balanced termination - the positive capacitance cancels the negative capacitance. Good ground bar construction typically results in the Scd21 performance of the termination at about -25dB.

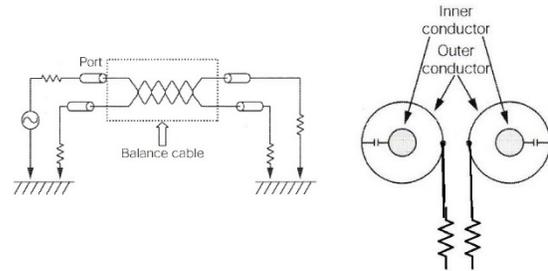


Figure 5. Balanced transmission line

4K2K Display Graphics Connector – Mitigating EMI

Shielding the Termination

After the prepared wire assembly is attached to the plug housing, a top metal shell is then applied over the area where the coaxial wires are terminated.

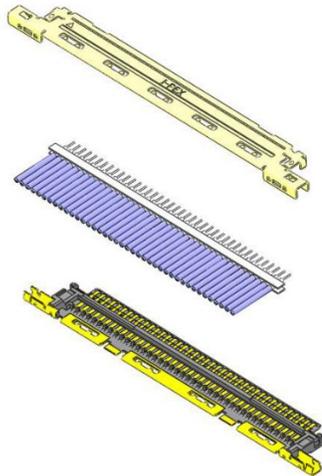


Figure 6. Plug shells to shield the termination area

The shell is then soldered to the grounding bar both on the top and bottom of the plug connector assembly.

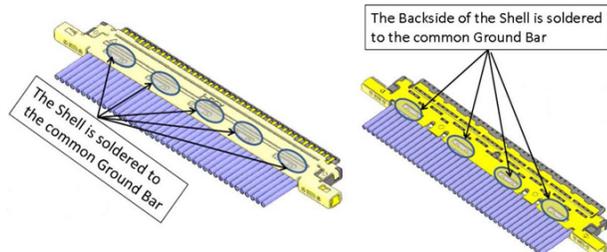


Figure 7. Shells soldered to ground bar – top and bottom

The shield grounding path goes through the connector's shells from the coaxial wire shields which are attached to the ground bar which is attached to the plug shell. The plug shell engages with the receptacle shell as shown in Figure 8, which takes the shortest path to ground along the span of the receptacle bottom contacts shown in Figure 9. The short ground paths enable good high-speed signal transfer performance.

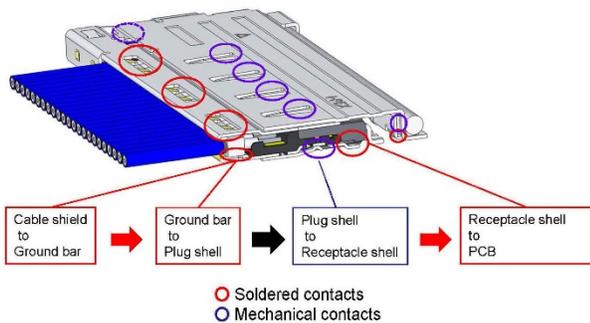


Figure 8. Mated set interconnections to ground

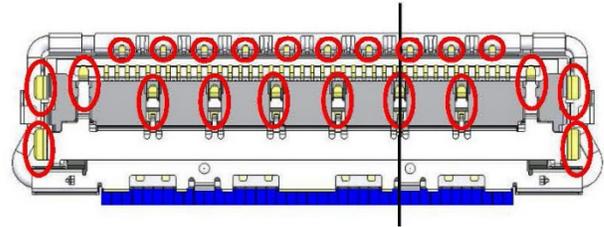


Figure 9. Bottom of receptacle shell ground contacts

Clearance between the shielding lid/bar and the solder tails was considered as a possible area where shorting could occur. This design (Figure 10) ensures an absolute clearance minimum of 1.7 mm.

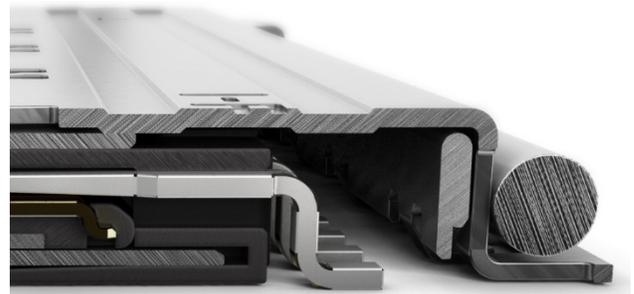


Figure 10. Clearance prevents shorts

Using CST Microwave Studio as shown in Figure 11, an electric field strength analysis was performed to compare the graphics connector's radiation with and without the solder tail shielding.

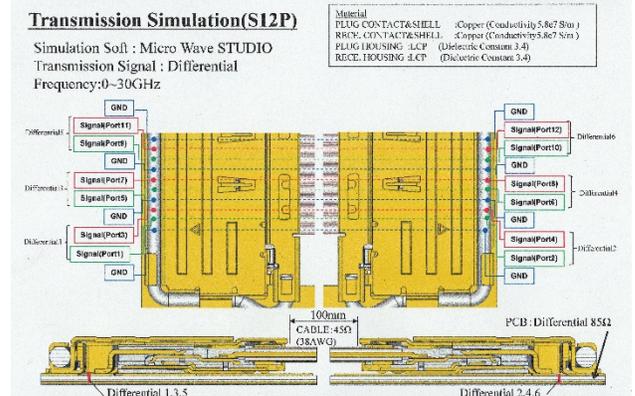


Figure 11. Simulation set-up

The 3-lane simulation was performed near one side at the locations of Pins 2 and 3, and also near the middle of the connector at Pins 20 and 21. A third location was analyzed between the shield cover ground spring fingers near Pins 8 and 9. The setup conditions for the simulation are shown in Figure 11 using the GSSGSSGSS signal assignment pattern.

4K2K Display Graphics Connector – Mitigating EMI

A comparison of the E-fields strength when transferring a stressful 20 GHz signal through the graphics connector with the solder-tails covered by the shielding lid and without the solder-tails shielded is shown in Figure 12.

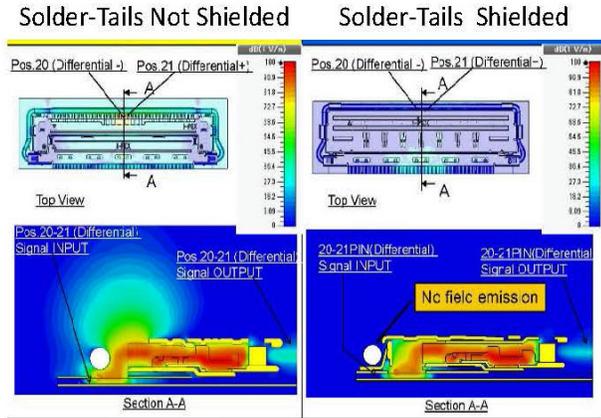


Figure 12. Simulated E-Fields at 20 GHz

The E-Field simulation for the three locations positions 20-21, 2-3 and between the ground springs for the connector are shown in Figure 13 at 2.4 GHz frequency (~5 Gbps) and Figure 14 at 5 GHz (~10 Gbps).

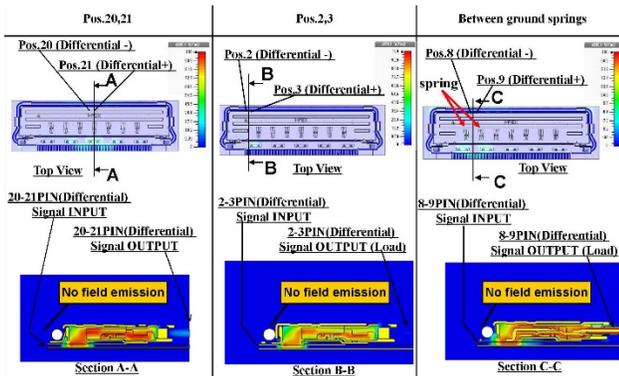


Figure 13. 2.4 GHz E-Field strength

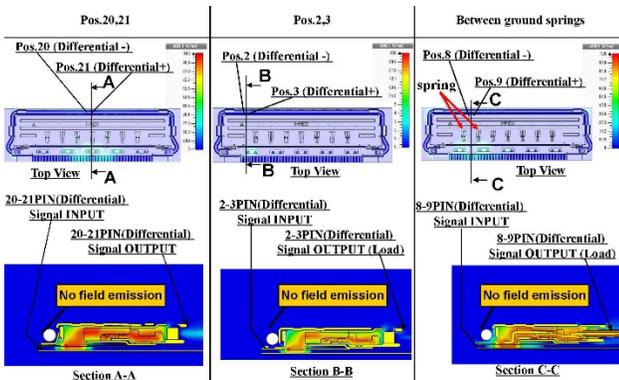


Figure 14. 5GHz E-Field strength

The simulation shows that no E-fields radiated from the graphics connector when the solder-tails are covered by the shielding lid.

Data rates are related to frequency by the term “Nyquist frequency.” The HBR2 5.4 Gbps Nyquist is 2.7 GHz which is the same frequency as some LTE carrier bands. When located in close proximity to the LTE radio/antenna ~2.7 GHz carrier signal, the unshielded solder tails of the graphics connector radiates a Nyquist frequency of ~2.7 GHz. Interference between Nyquist frequency radiation from the connector solder tails and the LTE wireless radio/antenna carrier frequency ~2.7 GHz causes the communications links to fail.

The EMI concerns are solved. The signal integrity performance analysis can now be addressed. The characteristic impedance profile as analyzed in the time domain and scattering parameters (S-parameters) are used for analysis of such parameters as Insertion Loss, Return Loss, Far End Crosstalk, Near End Crosstalk, and as previously discussed the Differential to Common Mode Conversion (Scd21) all in the frequency domain. A 3-differential lane analysis as shown in Figure 11 produces a s12p file that can be shared by Touchstone format.

Impedance Continuity of the Graphics Connector

The superior ground structure of the graphics connector enables the eDP HBR2 5.4 Gbps differential signals to transfer through the connector with a very small amount of impedance discontinuity. Typically the 5.4 Gbps data rate would equate with a rise time of $T_r=64ps$ (20%-80%) but since $T_r=40ps$ (20%-80%) is suggested by USB-IF for the 10 Gbps data rate the performance analysis used $T_r=40ps$ (20%-80%) as shown in Figure 15.

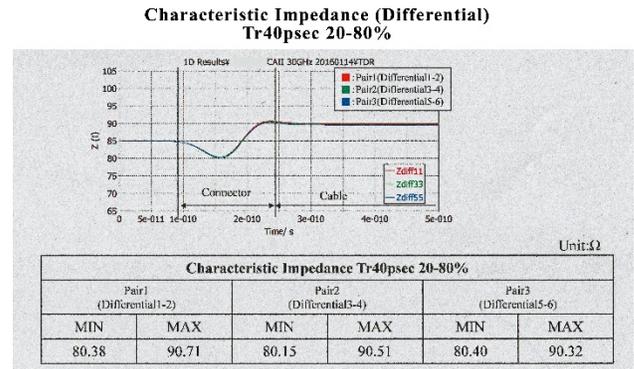


Figure 15. Impedance Discontinuity (TDR)

The average impedance discontinuity through the connector at 10 Gbps (5 GHz) is $\sim\Delta 10.2$ ohms. This result indicates that the connector should have good return loss (low reflections) and good crosstalk (high isolation) performance. A transmission channel that has large impedance path variations (discontinuities) exhibits poor return loss and crosstalk. Differential to common mode conversion is also expected to be worse.

4K2K Display Graphics Connector – Mitigating EMI

Insertion Loss and Return Loss (s12p)

The insertion loss for a 100mm long terminated cable using micro-coaxial wire AWG38 with 45ohm controlled impedance is shown in Figure 16. For example, at 5 Gbps (2.5 GHz) the IL is < 2dB.

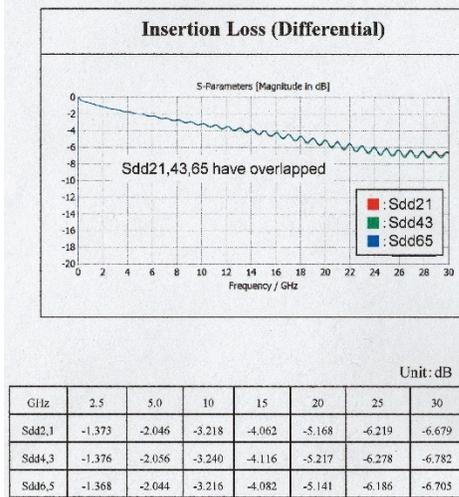


Figure 16. 100 mm Graphics cable simulation

The return loss for the 100 mm long cable shown in Figure 17 becomes unacceptable when it gets worse than -10dB (above 12 GHz). The shorter the cable the worse the return loss since the return path for the reflection is shorter.

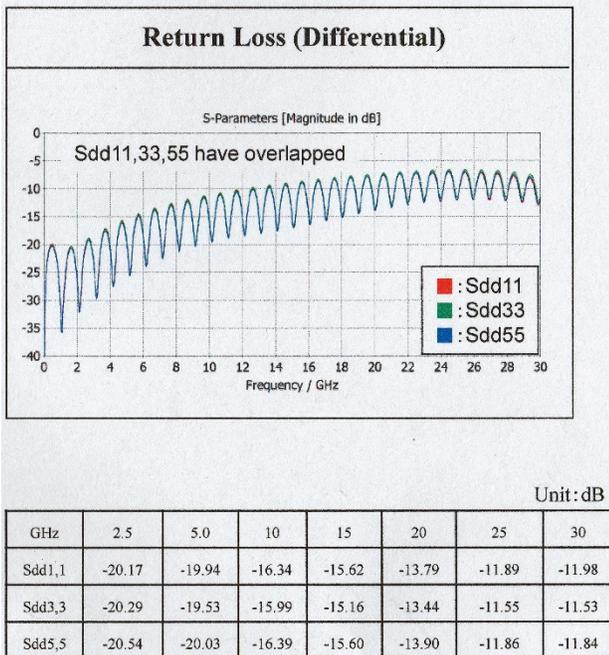


Figure 17. 100 mm Graphics cable simulation

The simulated crosstalk performance for the cable shown in Figure 18 also appears to have good results.

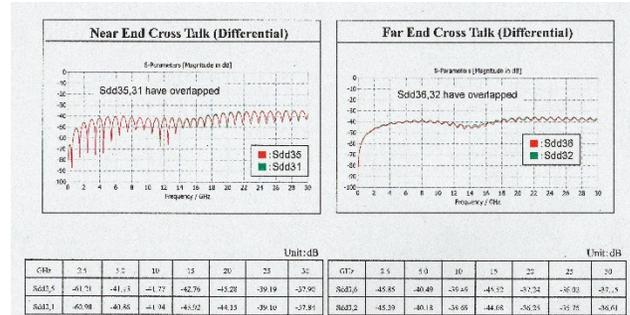


Figure 18. 100 mm Cable crosstalk

The eDP graphics connector design with an integrated locking shielding lid has the feature set to support the higher resolution display assemblies as the data rate continues to accelerate.

References

1. I-PEX, "IER-001-07244-00," *Simulation Transmission*, January 15, 2016
2. I-PEX, "IER-001-06877-00," *Grounding Bar Design*, May 29, 2015.
3. David E. Bockelman and William R. Eisenstadt, "Combined Differential and Common-Mode Scattering Parameters" IEEE Vol.43, No.7, July1955

Authored by Gregory A. Young, I-PEX Connectors



I-PEX Connectors
 2305 Donley Drive, Suite 110
 Austin, TX 78758
 512-339-4739
www.i-pex.com

© DAI-ICHI SEIKO Co., Ltd. 2016. All rights reserved.

I-PEX and CABLINE are trademarks of DAI-ICHI SEIKO Co., Ltd. All other trademarks are owned by their respective companies.

Disclaimer

All specifications of the products shown here are subject to change without notice. DAI-ICHI SEIKO Co., Ltd., assumes no responsibility for any inaccuracies or obligation to update information on these documents.